External reset

SD025 | Posted on April 2, 2021 | Updated on May 27, 2025



Benoît STEINMANN Software Team Leader imperix • in

The external reset block provides a signal that can be wired to the external reset input of PID controllers to keep the integrator at reset when the target is not in OPERATING state.

This block has been kept for legacy in Simulink. Its output is equal to the "reset" output of the <u>Core state block</u>.