Grid-Following Inverter (GFLI)

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Table of Contents

- What is a Grid-Following Inverter?
- Control of the Grid-Following Inverter
 - o Grid synchronization
 - Grid current control
 - PWM activation
- TPI 8032 implementation
- Software resources
 - Experimental results
- To go further

This technical note introduces the working principle of a Grid-Following Inverter (GFLI) and presents an implementation example built with the <u>TPI 8032</u> programmable inverter.

What is a Grid-Following Inverter?

Grid-Following Inverters (GFLI) and Grid-Forming Inverters (GFMI) are two basic categories of grid-connected inverters. Essentially, a grid-following inverter works as a current source that synchronizes its output with the grid voltage and frequency and injects or absorbs active or reactive power by controlling its output current. In contrast, a grid-forming inverter works as a voltage source that sets the amplitude and frequency of the grid, as introduced in <u>Grid-Forming Inverter</u>.

Thanks to the advantages of simplicity and relatively low price, grid-following inverters are widely used in grid-connected applications, such as renewable energy generation, energy storage, electric vehicle charging, etc. Compared to grid-forming

inverters, grid-following inverters can achieve faster power control and response, and also avoid some technical challenges such as synchronization between parallel-operating inverters.

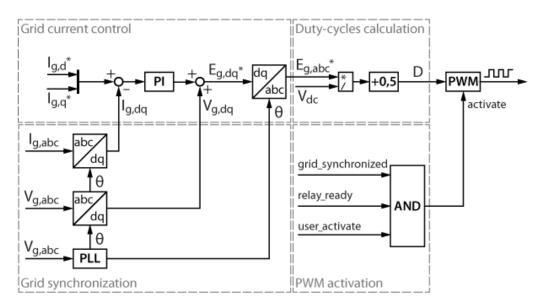
Control of the Grid-Following Inverter

The control objective of a Grid-Following Inverter is usually to control the active and reactive power injection to the grid. In a rotating reference frame (dq) synchronized with the grid voltage, the active and reactive power can be expressed as:

$$P = rac{3}{2} \cdot V_{g,d} \cdot I_{g,d}$$

$$Q = rac{3}{2} \cdot V_{g,q} \cdot I_{g,q}$$

If the grid voltage amplitude $V_{g,d}$ is constant, the control of the active and reactive power can be simplified with the control of the grid currents in the dq-frame. In addition to the current control loop, some additional functionalities are also needed, such as synchronization with the AC grid and proper activation of the PWM outputs, as depicted in the control diagram below.



Control diagram of the Grid-Following Inverter example

Grid synchronization

An accurate synchronization system is required to track the grid's phase and frequency. This note uses an <u>SRF PLL</u> as an example, which is a simple and widely used solution for synchronization with the three-phase grid. Another possible technique is introduced in <u>SOGI PLL</u>, which has better dynamic performance and can work with a single-phase grid, at the cost of a more complex implementation.

Grid current control

In this note, the active and reactive power flow is directly controlled by the current reference $I_{g,d}$ and $I_{g,q}$ in the rotating reference frame (dq). The grid current components are controlled by two PI controllers in the grid-synchronous frame (dq) using the same technique as introduced in <u>Vector current control</u>.

The plant model of the current controller contains multiple terms, in part due to the multi-stage filter of the TPI 8032. However, for the tuning purpose of the current controller, this plant model can be simplified to a first-order delay representing the actuator, and a sole inductor. The resulting plant transfer function is therefore:

$$G_{plant}(s) = rac{1}{1 + sT_d} \cdot rac{1}{R_g + sL_g}$$

The control parameters $K_{p,I}$ and $K_{i,I}$ can, for instance, be tuned using the Magnitude Optimum (MO) criterion, considering the plant model above and the parameters L_g and R_q extracted from to the <u>TPI 8032 datasheet</u>.

$$K_{p,I}=rac{L_g}{2T_d}$$

$$K_{i,I} = rac{R_g}{2T_d}$$

Lg	1050 μΗ
Rg	54 mΩ

Plant parameters

The parameter T_d represents the sum of all the small delays in the system, such as the computation and the modulation delays. The product note <u>Time delay determination for closed-loop control</u> explains how to determine the total delay of the system. In this example, the cycle delay is shorter than half a control period ($T_{cy} < 0.5T_s$), and a triangular carrier is used for PWM modulation. The delay total delay T_d is computed as follows:

• Sensing delay: neglected ($\approx 500 \, \mathrm{ns}$)

• Control delay: $T_{d,ctrl} = 0.5T_s$

ullet Modulator delay: $T_{d,\mathrm{PWM}} = 0.5 T_{sw}$

Switching delay: neglected, sub-microsecond

Please also note that if <u>synchronous averaging</u> is enabled, which is the case by default in <u>TPI ADC</u> blocks, an additional delay of $0.5T_{sw}$ has to be considered, due to

the averaging of the measured quantities over one switching period. When considering $T_s=T_{sw}$, the total delay is therefore:

$$T_d = T_{d,ctrl} + T_{d,PWM} + T_{d,avg} = 1.5T_s$$

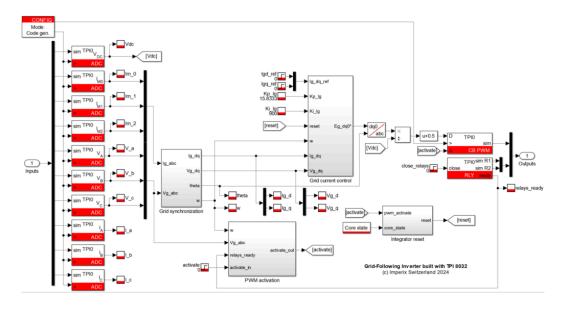
PWM activation

The PWM output is only activated when:

- PLL is synchronized with the grid;
- Relays are closed and ready to operate;
- The tunable parameter activate is set to '1' by users.

TPI 8032 implementation

The Grid-Following Inverter control has been implemented in Simulink.



Grid-Following Inverter control implementation in Simulink

Software resources

The following zip files contain examples of control using MATLAB Simulink and PLECS.

TN167_Grid_Following_Inverter_SimulinkDownload

TN167_Grid_Following_Inverter_Simulink.zip

Download

TN167_Grid_Following_Inverter_PLECS.zip

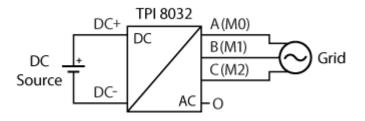
The plant model is built with the Imperix Power library for fast and accurate simulation of imperix power products. For more information on the Imperix Power library, please read PN150 – Getting started with Imperix Power library. Imperix Power library requires ACG SDK 2024.2 or a later version. To update the ACG SDK, please go to imperix.com/downloads/.

The minimum requirements are:

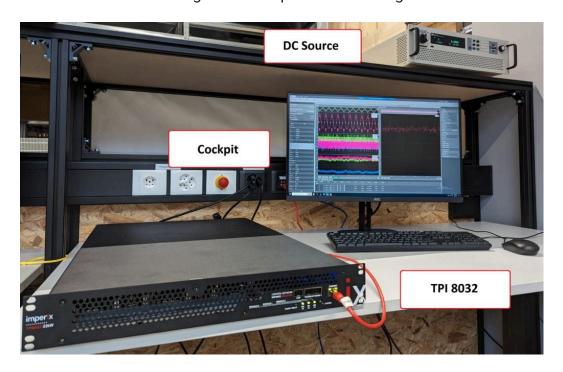
- Imperix ACG SDK 2024.2 or newer, available here
- MATLAB Simulink R2016a or newer.
- Plexim PLECS 4.5 or newer
- For simulation only: Simscape Electrical

Experimental results

The experiment has been carried out on a TPI 8032 with a DC source and AC grid. The wiring scheme and the experiment setup are shown below.



Grid-Following Inverter experimental wiring scheme



Grid-Following Inverter experimental setup

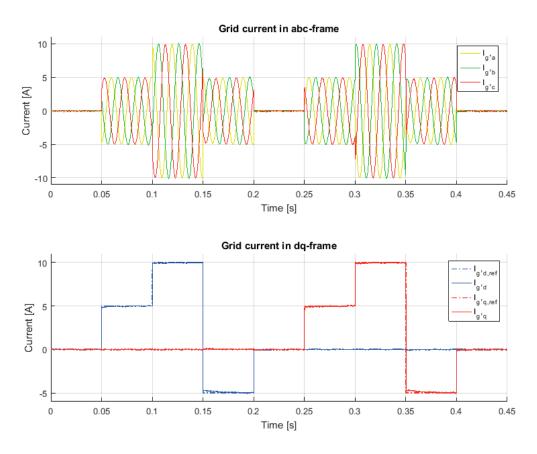
The operating conditions are:

Control frequency [kHz]	50
DC bus voltage [V]	750
RMS grid voltage (phase to neutral) [V]	
Grid frequency [Hz]	50

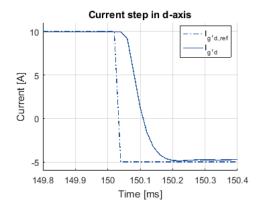
Experiment conditions

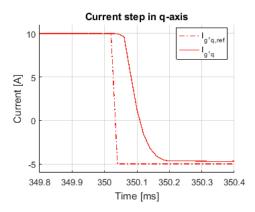
The experimental results are presented below with steps performed on the d-axis and q-axis current reference. It can be seen that the current reference is rapidly tracked by the controller during the whole test.

Besides, the controller also has good performance in decoupling d-axis and q-axis currents. When there is a current step in one axis, the current in the other axis remains unaffected.



The controller's time-domain performance can be further verified by examining one step on the d-axis or q-axis more closely. As seen in the following figure, the settling time is around 200 us. The controller has fast performance with no overshoot.





To go further

The TPI 8032 can be easily programmed to fit any purpose. More examples of TPI in microgrid applications are available in the knowledge base, such as:

- Active Front End
- Grid-Forming Inverter