Virtual impedance for droop control

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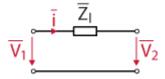
This note introduces the virtual impedance method for power decoupling in resistive lines. An implementation example is provided to validate the virtual impedance method with droop control featuring the <u>programmable inverter TPI 8032</u> and <u>ACG SDK</u>.

An overview of the hardware architecture and detailed instructions on how to program the device are addressed in <u>getting started with the TPI 8032</u>.



Power coupling issue in resistive lines

<u>Droop control</u> is a well-established communication-less control strategy for regulating the power-sharing between <u>Grid-Forming Inverters (GFMI)</u>. However, the idea of droop control is based on the fact that the active and reactive power flow can be decoupled in an inductive transmission line, which is not always true, especially in low-voltage transmission grids.



Considering the single-phase equivalent circuit for the symmetrical three-phase transmission line, the active and reactive power flow in a transmission line can be calculated by:

$$P = 3rac{V_{1}}{R_{l}^{2} + X_{l}^{2}}[R_{l}(V_{1} - V_{2}\cos\delta) + X_{l}V_{2}\sin\delta]$$

$$Q=3rac{V_1}{R_l^2+X_l^2}[X_l(V_1-V_2\cos\delta)-R_lV_2\sin\delta]$$

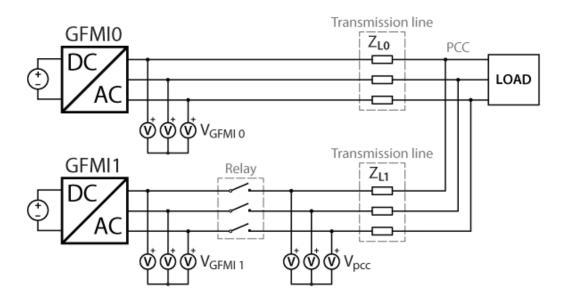
When the transmission line is mostly inductive ($R_l \approx 0$), and the phase angle is small enough ($\sin(\delta) \approx \delta, \cos(\delta) \approx 1$), the active power P and reactive power Q can be further simplified as [1]:

$$Ppprox 3rac{V_1V_2}{X_l}\delta$$

$$Qpprox 3rac{V_1(V_1-V_2)}{X_I}$$

Based on the equations above, the active and reactive power flows can be fully decoupled and controlled independently. However, if the transmission line is mostly resistive $(R_l \neq 0)$, the above approximations no longer hold, and both active and reactive power depend on the angle and the voltage variation along the line.

Considering a similar setup as introduced in <u>Parallel operation of GFMIs</u>, two GFMIs running in parallel are considered with a load, connected at the other end of a mainly resistive line. With conventional droop control, a load step ΔP_{load} determines a variation of the frequency reference ω^* . But with a resistive load, the reactive power Q is also affected, causing the change of the voltage reference V^* . Again, since the active power P also depends on the voltage, P will change further, leading to oscillations and possible instability of the system.



System under study: two parallel GFMIs with load

Working principle of the virtual impedance method

To avoid the coupling issue between active and reactive power, a virtual impedance can be added to the conventional droop control. The working principle of the virtual impedance method is to mimic a line inductance by subtracting the voltage reference V^* by a virtual voltage drop $V_{\rm v}$.

$$V_{
m v} = L_{
m v} rac{{
m d}I}{{
m d}t}$$

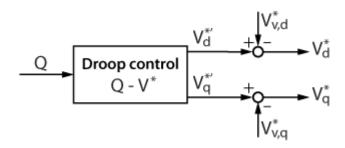
Where $L_{\rm v}$ is the virtual inductance.

To avoid the unwanted noise amplification of numerical derivation, approximation methods have been proposed in [2] by calculating the virtual voltage drop in the

rotating reference frame (dq0):

$$V_{ ext{v},d} = -L_{ ext{v}} \omega I_q$$
 $V_{ ext{v},q} = L_{ ext{v}} \omega I_d$

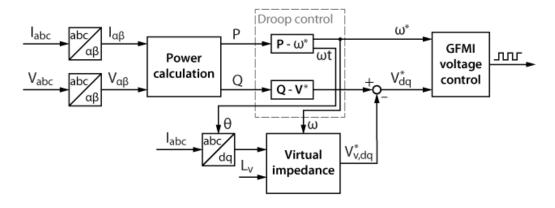
The corresponding control diagram is as follows:



Concept of the virtual impedance method

In contrast to a conventional droop control, an active power variation determines a voltage reference variation through a virtual inductance. Moreover, the virtual voltage drop allows the conceptual decoupling of active and reactive power. Therefore, stability is ensured by using the droop control with virtual inductance. The effect of the $L_{\rm v}$ value is studied in the experimental result section.

With the addition of the virtual impedance, the whole diagram of the droop controller becomes:



Droop control with virtual impedance

Virtual impedance implementation with imperix ACG SDK

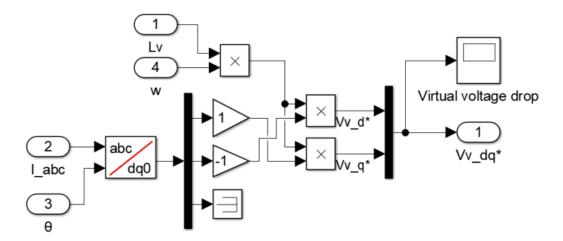
The control models available here after are implemented in Simulink using the imperix <u>ACG SDK</u> blockset. The models can both simulate the behavior of the system in an offline simulation and generate code for real-time execution on the <u>TPI 8032</u>.

An introductory guide regarding the TPI is addressed in <u>Getting started with the TPI</u> <u>8032</u>. To run these models, the minimum requirements are:

- Imperix ACG SDK 2024.2 or newer.
- MATLAB Simulink R2016a or newer.
- For simulation only: Simscape Electrical

<u>Download TN171_Virtual_impedance_Simulink</u>

The Simulink implementation of the virtual impedance is shown below.



Simulink model for the virtual impedance

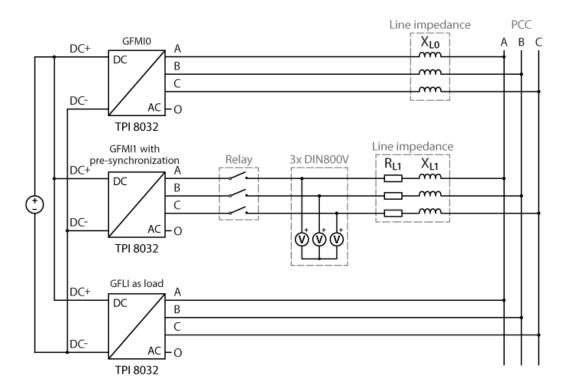
Experimental setup

The experimental validation of the virtual impedance method is carried out with a similar setup as in <u>Parallel operation of GFMIs</u> with two TPIs running in parallel as GFMIs and one TPI as GFLI as an active load. The three TPIs are used in a master-slave configuration (connected with <u>SFP cables</u>), meaning that they are programmed from the same Simulink model. The GFMI0 is connected to the PCC through inductors emulating an inductive transmission line, while the GFMI1 is connected through resistors and small inductors emulating a resistive transmission line. A relay controlled by a <u>GPO port</u> is used to connect GFMI1.

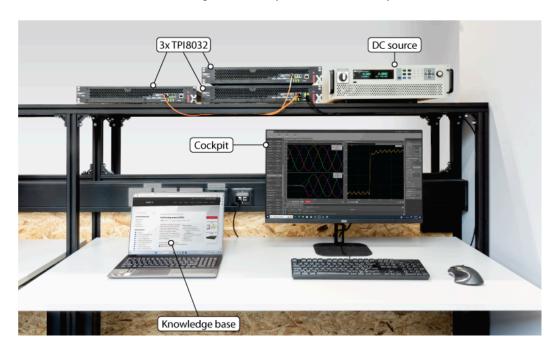
The required equipments are:

- 3x <u>TPI 8032</u> three-phase inverter
- ACG SDK toolbox for automated generation of the controller code from Simulink or PLECS
- 1x bidirectional DC power supply (800V)
- 6x inductors (3x 2.2mH and 3x 90μH)
- 3x resistors (here 1.6Ω)
- 1x three-phase relay

• All the necessary cables



Wiring of the experimental setup



Experimental setup with imperix products

The following table summarizes the operating conditions:

Parameter	Value
Control and switching frequency	50 kHz
Maximum active power P Maximum apparent power S Maximum reactive power Q	16 kW 22 kVA 15.1 kVar

Parameter	Value
Grid Voltage	400 VRMS
DC voltage Maximum RoCoF $ ho$	800 V 1 Hz/s
,	
Δf (1%)	3.14 rad/s
ΔV (10%)	32.5 V
Line inductance of GFMI0	2.2 mH
Line resistance of GFMI1	1.6 Ω
Line inductance of GFMI1	90 μH
APC droop coefficient m	1.9635e-04
APC - LPF cut-off frequency	0.3 Hz
APC - HPF cut-off frequency	0.1 Hz
RPC droop coefficient n	0.0022
RPC – LPF cut-off frequency	0.3 Hz

Experimental conditions

Based on typical line parameters taken from [3], the resistance and reactance of PVC-insulated multi-core copper conductor with a cross-sectional area of 4mm² are:

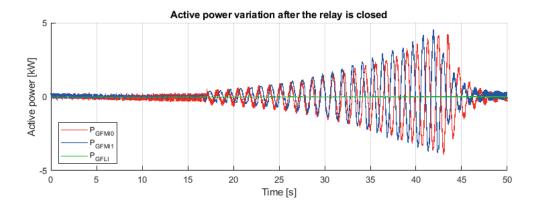
$$R_l = 5.8783 \quad \Omega/\mathrm{km}$$

$$X_l @ 60\mathrm{Hz} = 0.113 \quad \Omega/\mathrm{km}$$

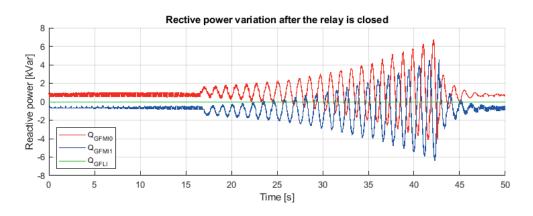
The lumped components used experimentally are approximately equivalent to a transmission line of 300m, which is realistic in a small low-voltage system.

Experimental results

The experiment is performed by running GFMI0 while GFMI1 is still unconnected. Then, GFMI1 is connected with prior pre-synchronization. As the first step, no virtual impedance is implemented on GFMI1. The active and reactive power flow after GFMI1 is connected are shown below. The experiment has to be stopped after 42 seconds to avoid excessive current.



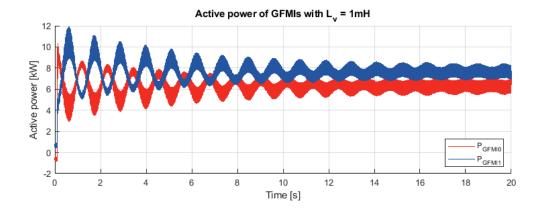
Active power variation after the relay is closed



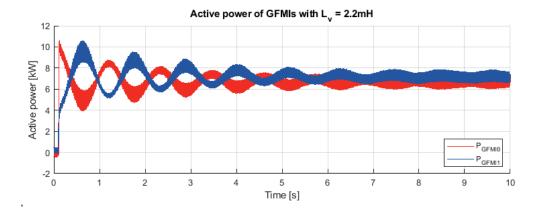
Reactive power variation after the relay is closed

Without the virtual impedance, oscillations of power exchange between the two GFMIs can be observed, leading to instability of the system.

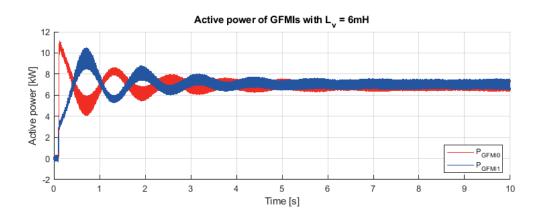
After adding the virtual impedance to GFMI1, the system's stability can be ensured. To study the effect of different virtual inductance values, an active power step of 15kW is performed on the load under different virtual inductance values.



Active power of GFMIs with Lv = 1mH



Active power of GFMIs with Lv = 2.2mH



Active power of GFMIs with Lv = 6mH

It can be observed from the results that for smaller virtual inductance value $L_{\rm v}=1\,{\rm mH}, X_l/R_l=0.2$, the effect of the line resistance cannot be fully eliminated. Oscillations happen when the load changes and still exists in the steady state. Besides, an inaccuracy of active power sharing can be observed. As the virtual inductance value increases, the effect of the line resistance gets mitigated. For large virtual inductance $L_{\rm v}=6\,{\rm mH}, X_l/R_l=1.17$, the oscillation in the steady state can be almost removed, and accurate active power sharing can be ensured. Obviously, the virtual inductance cannot be increased infinitely, since the induced virtual voltage drop along the line will cause the PCC voltage to decrease.

To go further

To address the lack of explicit inertia in the control, the inertial characteristics of synchronous generators can be emulated through a virtual synchronous generator control. This is addressed in <u>Virtual synchronous generator</u>.

Academic reference

- [1] K. De Brabandere, B. Bolsens, J. Van den Keybus, A. Woyte, J. Driesen and R. Belmans, "A Voltage and Frequency Droop Control Method for Parallel Inverters," in *IEEE Transactions on Power Electronics*, Jul. 2007
- [2] Y. W. Li and C.-N. Kao, "An accurate power control strategy for power-electronics-interfaced distributed generation units operating in a low-voltage multibus microgrid," in *IEEE Transactions on Power Electronics*, Dec. 2009.
- [3] Alfanar, "Low voltage power and control cables," https://www.alfanar.com/catalogs/cables_wires/LV_power_cables.pdf, Accessed Mar. 2024.